

Docket No. 220327US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Yasuhiko MATSUNAGA, et al.
SERIAL NO: NEW APPLICATION
FILED: Herewith
FOR: NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE

GAU:
EXAMINER:



INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:
Applicant(s) wish to disclose the following information.

REFERENCES

- ☒ The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- ☐ Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

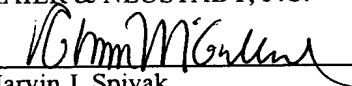
- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- ☒ Please charge any additional fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.


Marvin J. Spivak

Registration No. 24,913

C. Irvin McClelland
Registration Number 21,124



22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 10/01)

DOCKET NO.: 220327US2

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STATEMENT OF RELEVANCY

Reference AO (10-283788) on Form PTO-1449:

This reference shows similar gate bias voltages during programing (Fig. 4 and lines 92-100 of page 7).

Reference AP (8-279297) on Form PTO-1449:

This reference shows the original local self-boosting program method.

Form PTO 1449
(Modified)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY DOCKET NO.

220327US2

SERIAL NO.

NEW APPLICATION

LIST OF REFERENCES CITED BY APPLICANT

APPLICANT

Yasuhiko MATSUNAGA, et al.

FILING DATE

Herewith

GROUP

c997 U.S. PTO
10/090995

03/06/02

U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB CLASS | FILING DATE IF APPROPRIATE |
|---------------------|----|--------------------|------|------|-------|--------------|-------------------------------|
| | AA | | | | | | |
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FOREIGN PATENT DOCUMENTS

| | | DOCUMENT NUMBER | DATE | COUNTRY | TRANSLATION | |
|--|----|--------------------|----------|-------------------------------|-------------|----|
| | | | | | YES | NO |
| | AO | 10-283788 | 10/23/98 | Japan (with English abstract) | | X |
| | AP | 8-279297 | 10/22/96 | Japan (with English abstract) | | X |
| | AQ | | | | | |
| | AR | | | | | |
| | AS | | | | | |
| | AT | | | | | |
| | AU | | | | | |
| | AV | | | | | |

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

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|--|----|--|
| | AW | |
| | AX | |
| | AY | |
| | AZ | <input type="checkbox"/> Additional References sheet(s) attached |

Examiner

Date Considered

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.